

FIG. 1A (PRIOR ART)

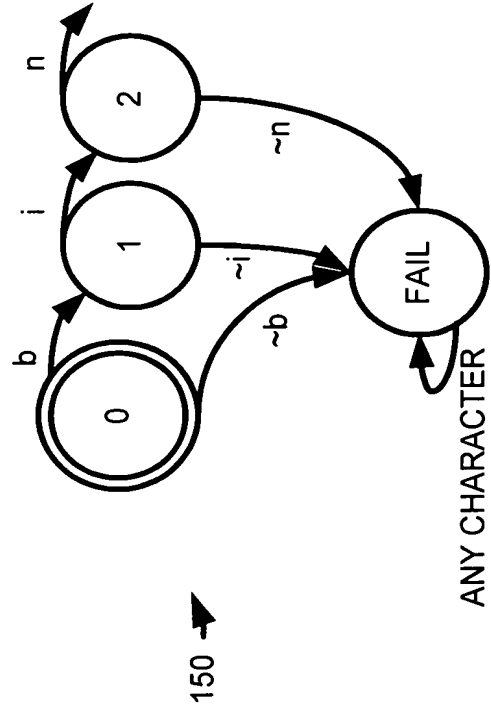


FIG. 1B (PRIOR ART)

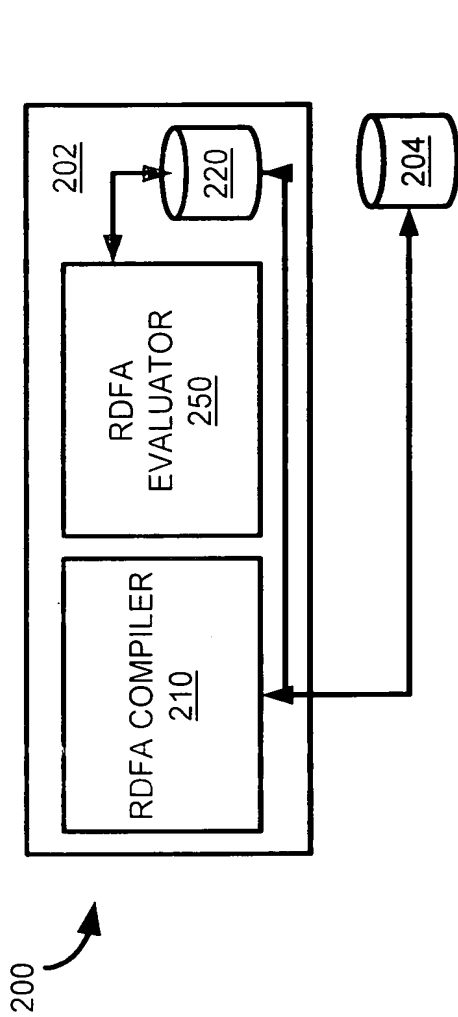


FIG. 2A

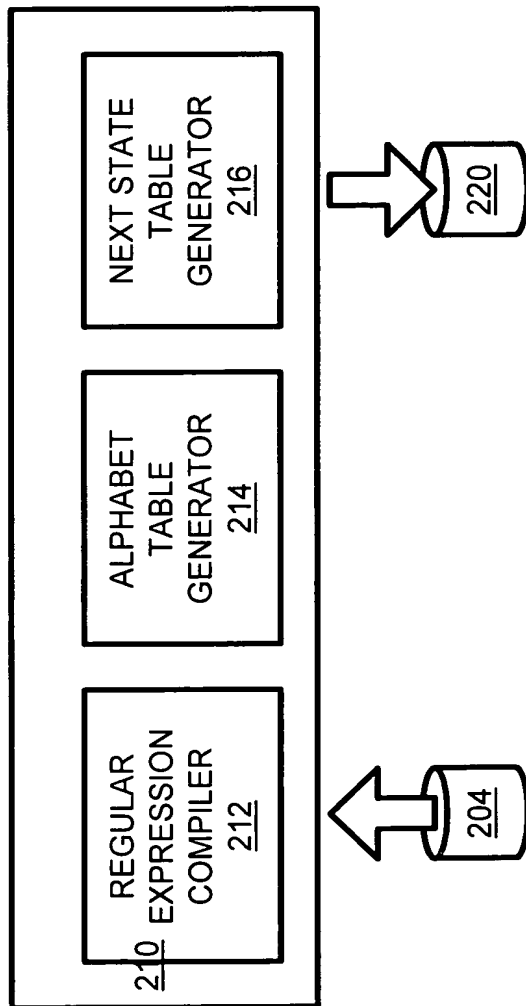


FIG. 2B

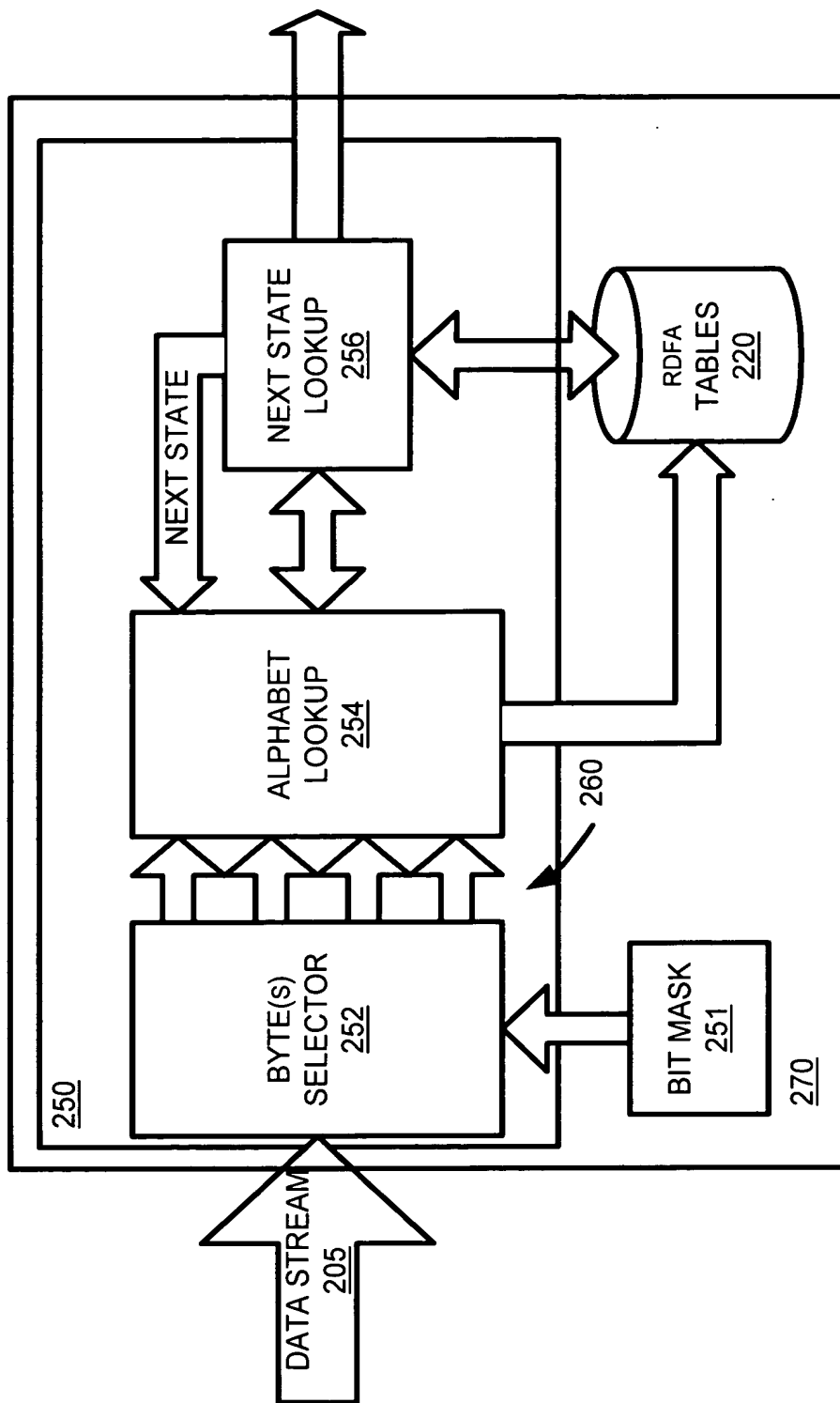


FIG. 2C

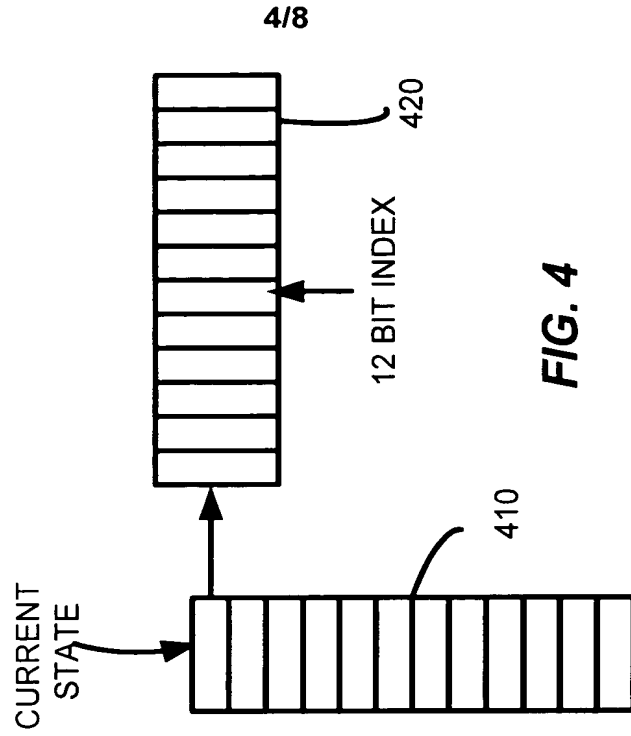
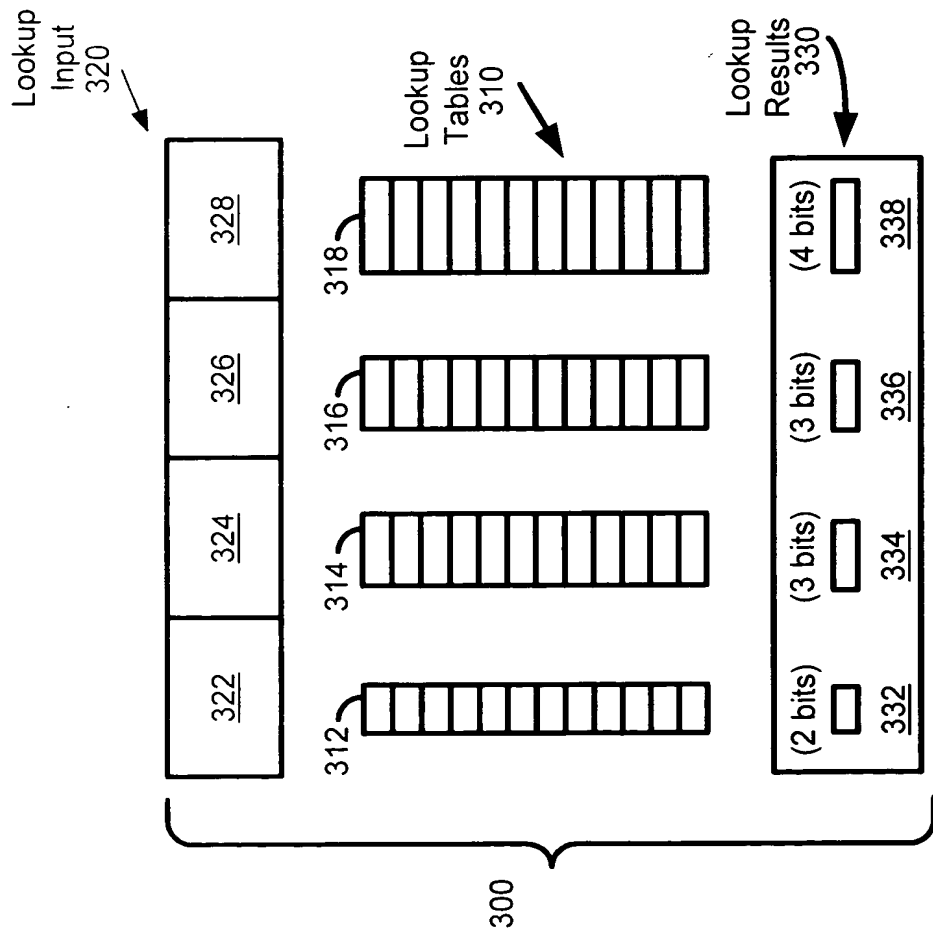


FIG. 4



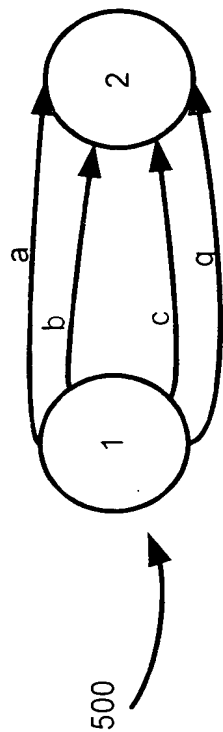


FIG. 5

650

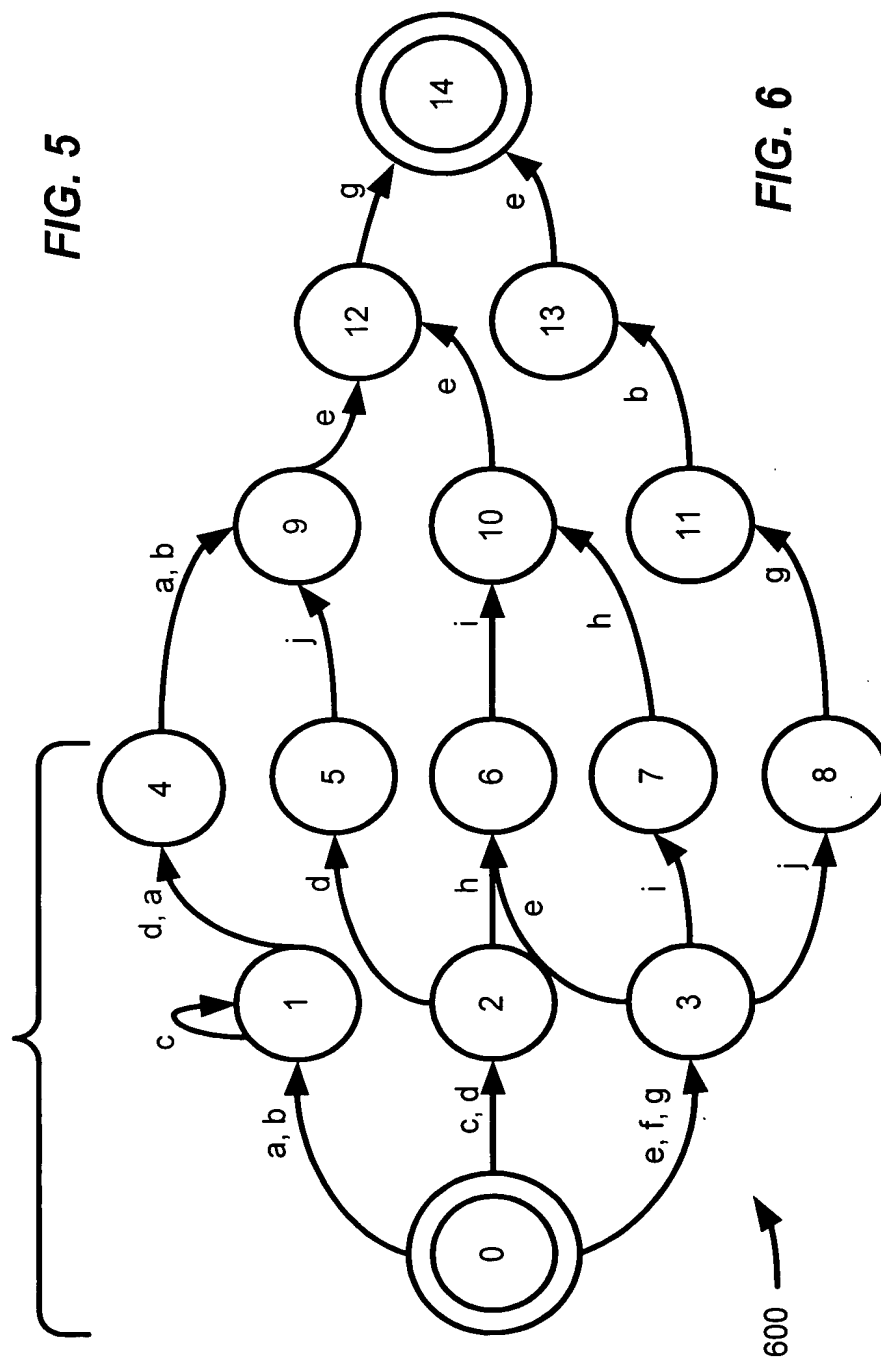


FIG. 6

Fig. 6A Illustration of number of states reachable by 2-closure.

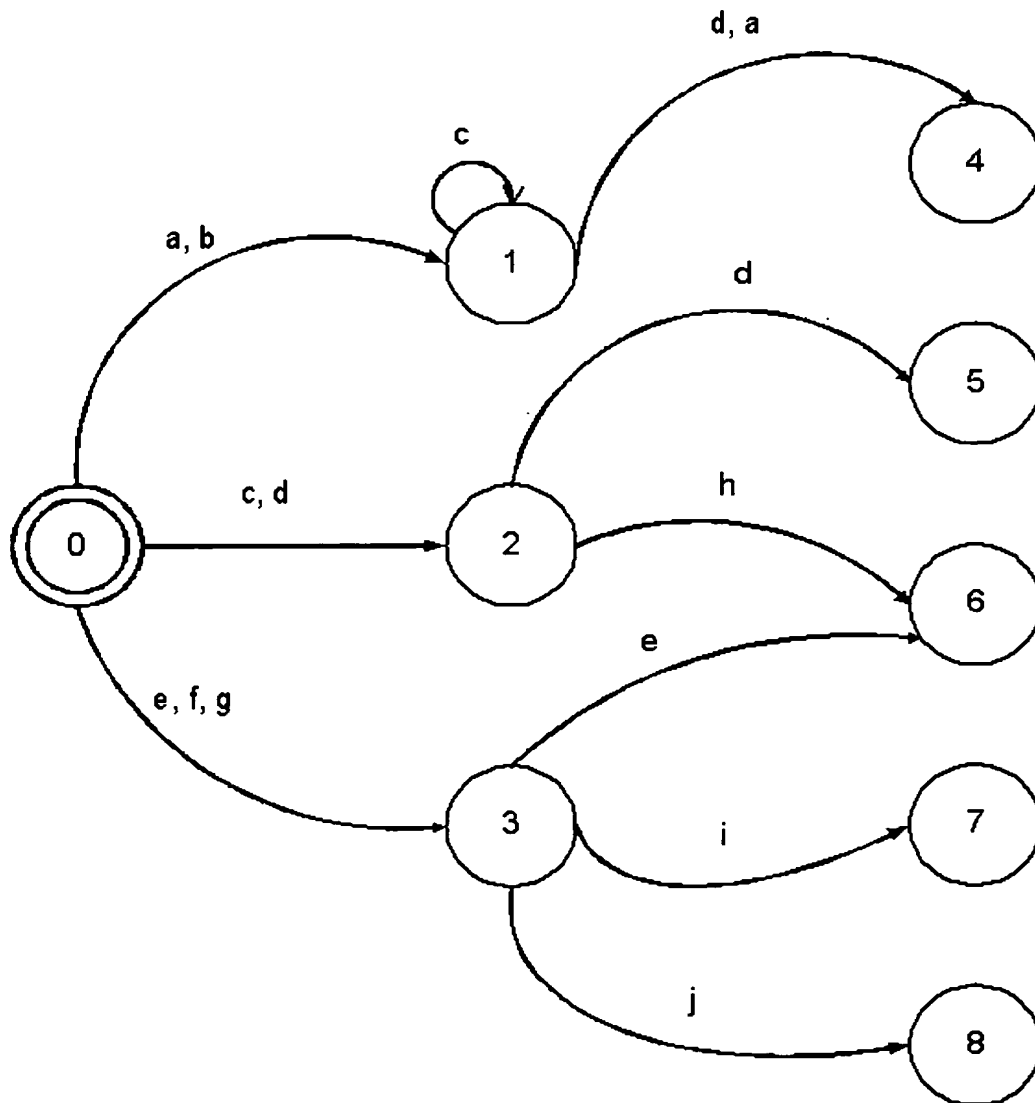


Fig. 7A DFA for processing 8 characters

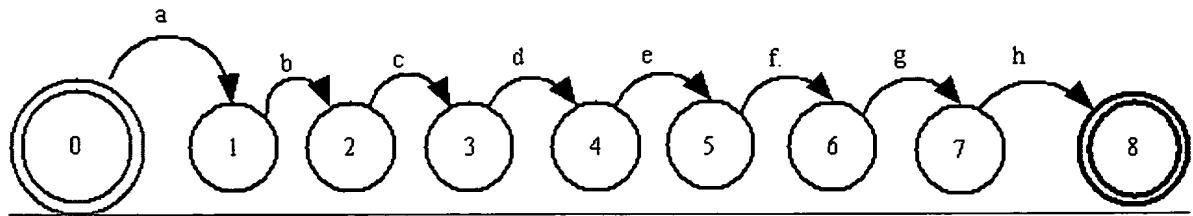


Fig 7B RDFA for processing 4 bytes in parallel.

